

FIG. 1

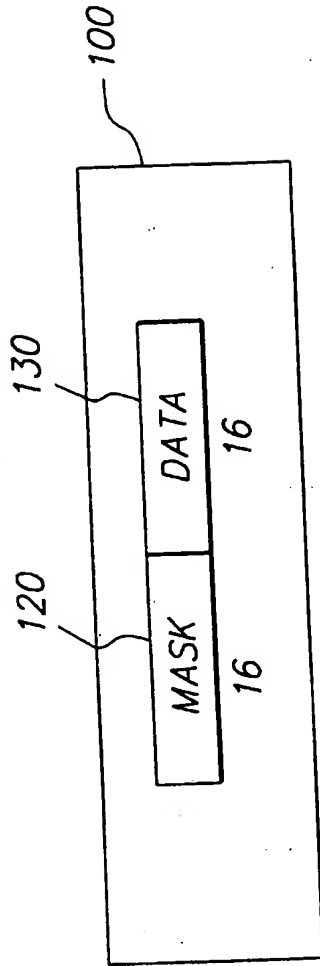


FIG. 2

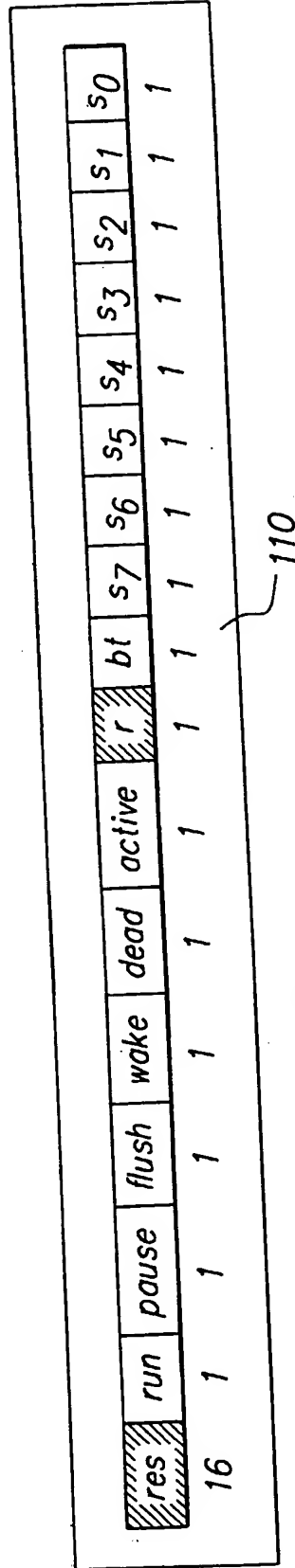


FIG. 3

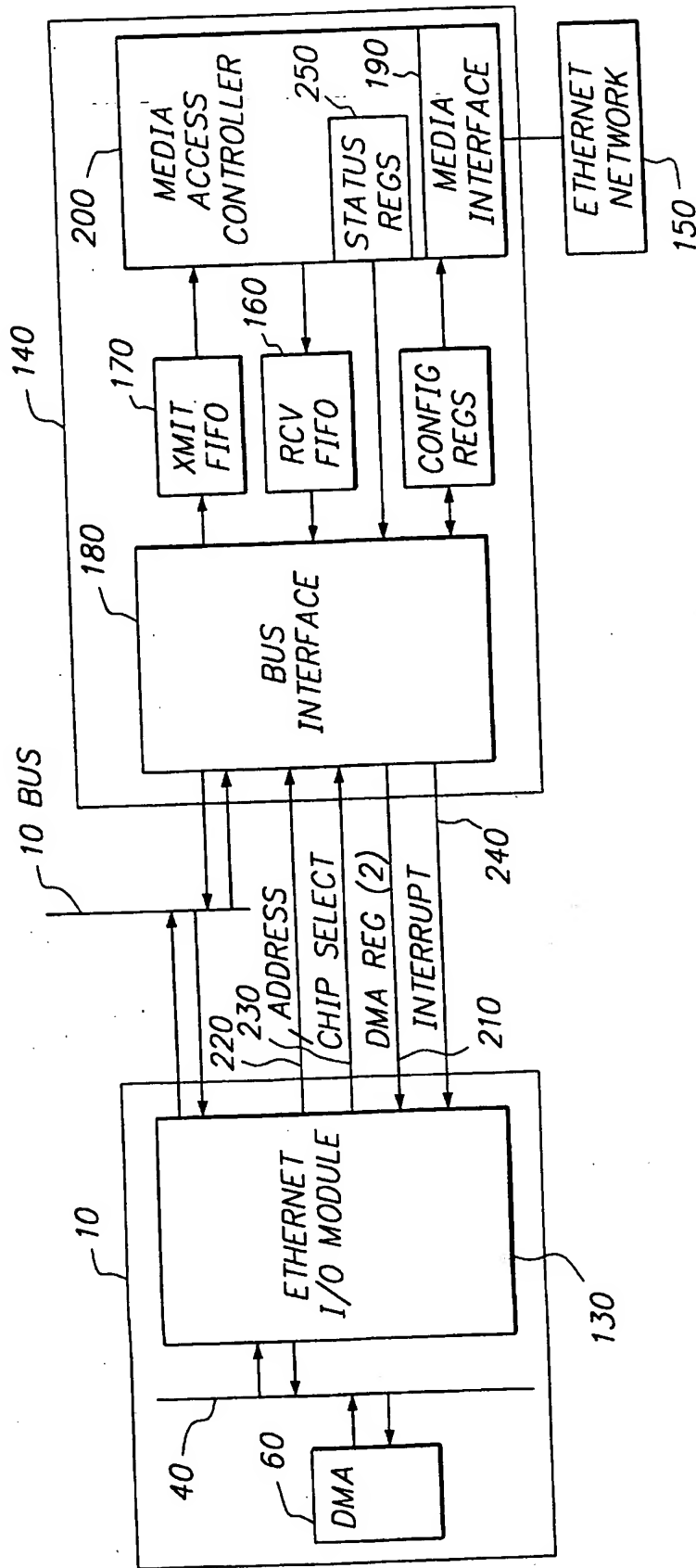


FIG. 4

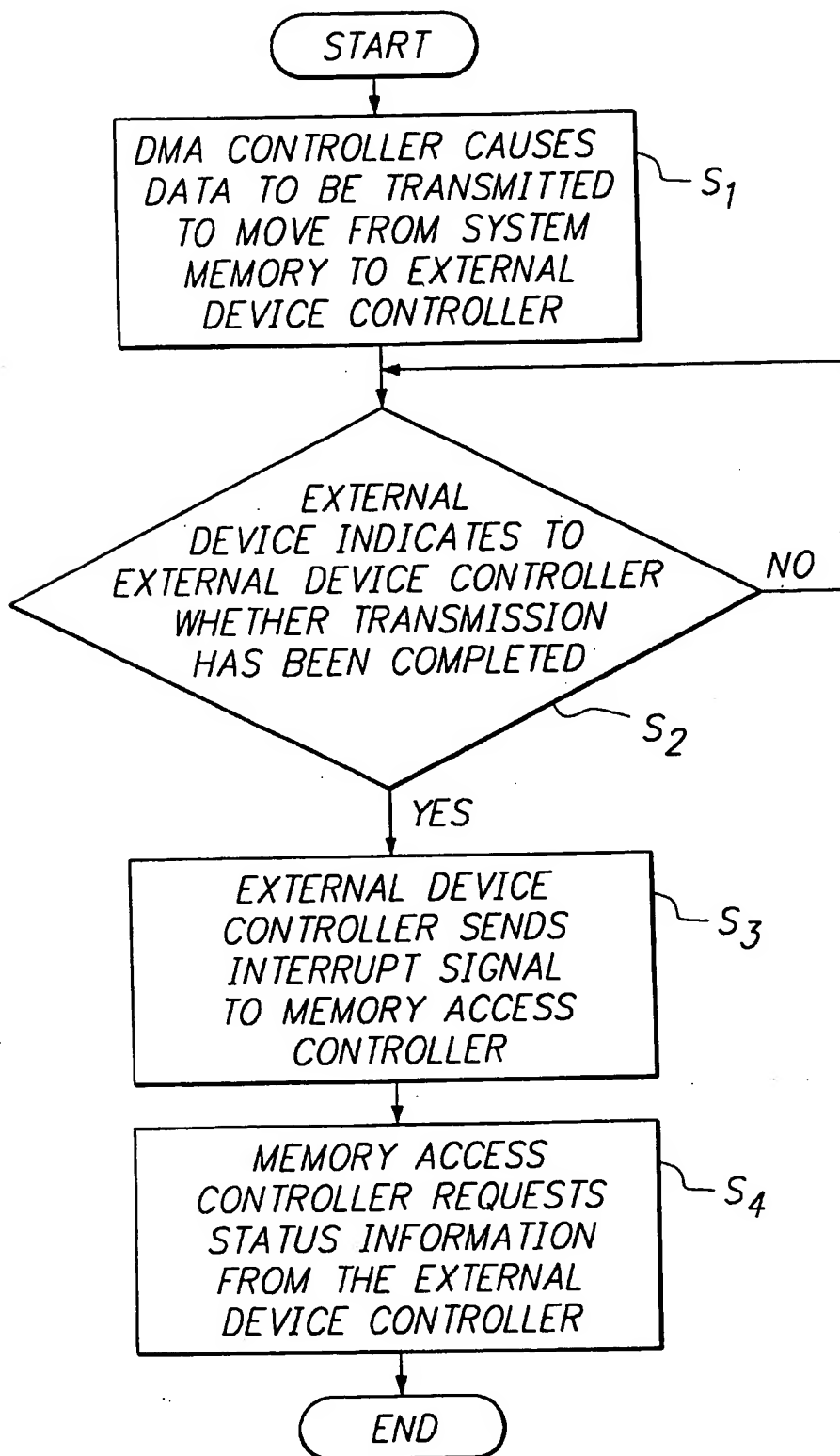


FIG. 5

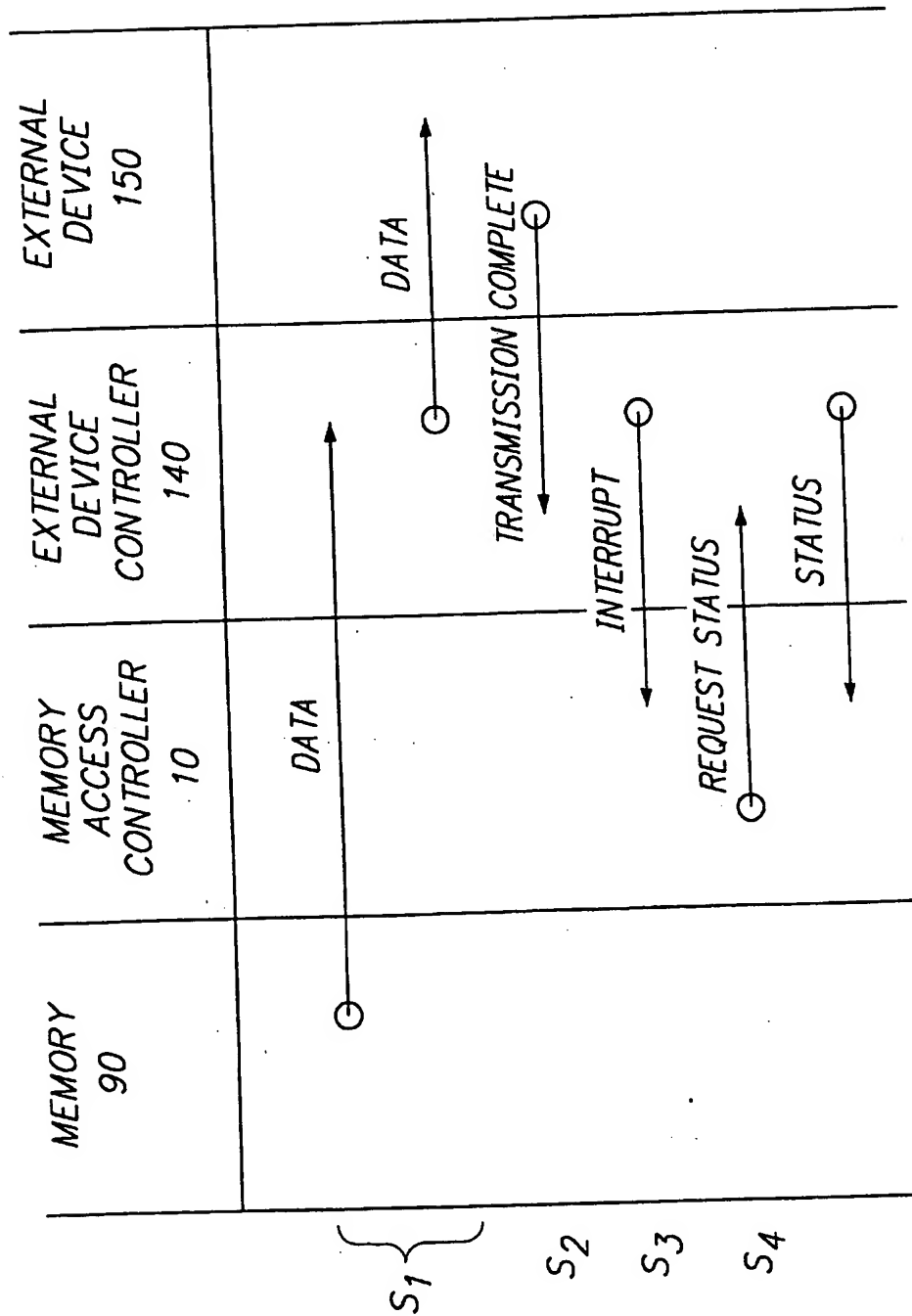


FIG. 6

<i>BIT</i>	<i>MEANING</i>
<i>S₇</i>	<i>ERROR</i>
<i>S₆</i>	<i>NOT IMPLEMENTED</i>
<i>S₅</i>	<i>MACE INTERRUPT</i>
<i>S₄...S₁</i>	<i>NOT IMPLEMENTED</i>
<i>S₀</i>	<i>WAIT (EXTERNALLY CONTROLLED)</i>

FIG. 7

<i>BIT</i>	<i>MEANING</i>
<i>S₇</i>	<i>NOT IMPLEMENTED</i>
<i>S₆</i>	<i>EOP (END OF PACKET)</i>
<i>S₅...S₁</i>	<i>NOT IMPLEMENTED</i>
<i>S₀</i>	<i>WAIT (EXTERNALLY CONTROLLED)</i>

FIG. 9

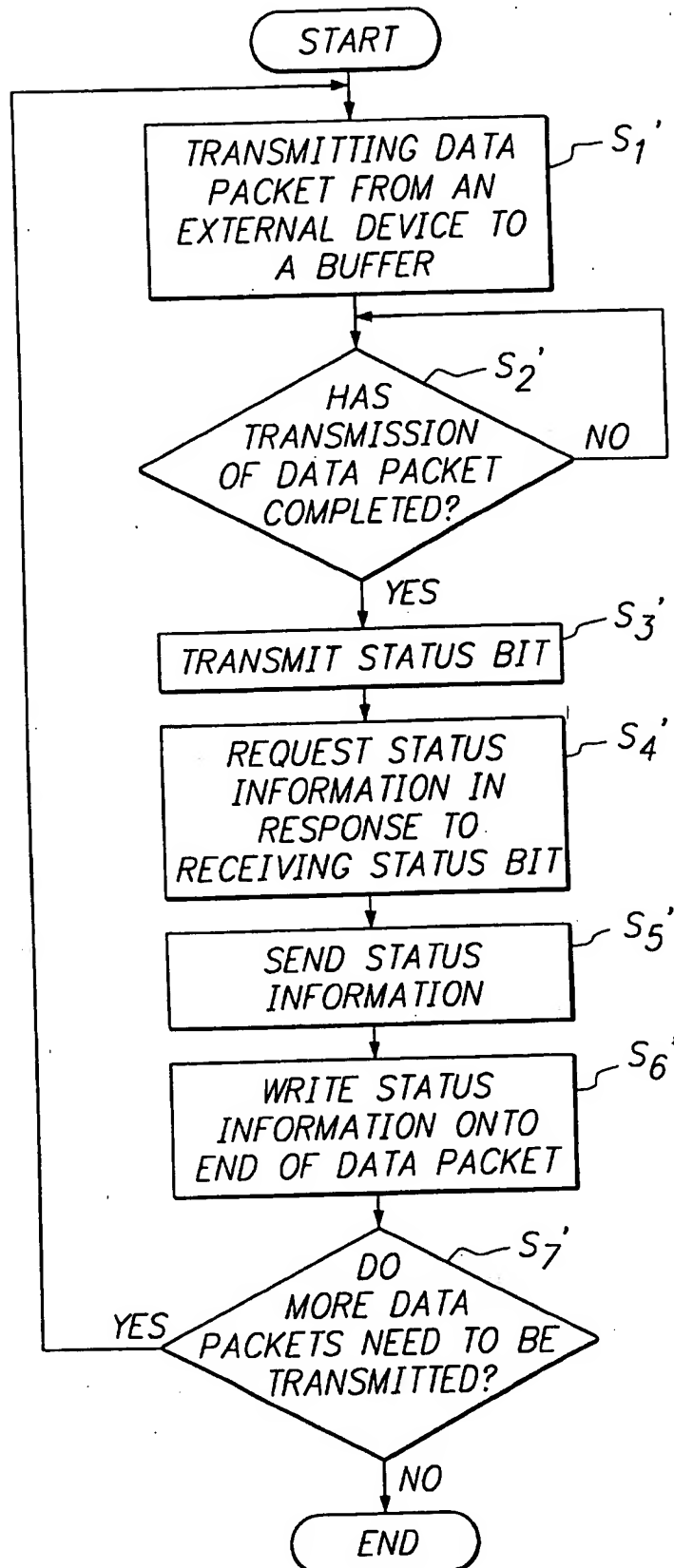


FIG. 8